

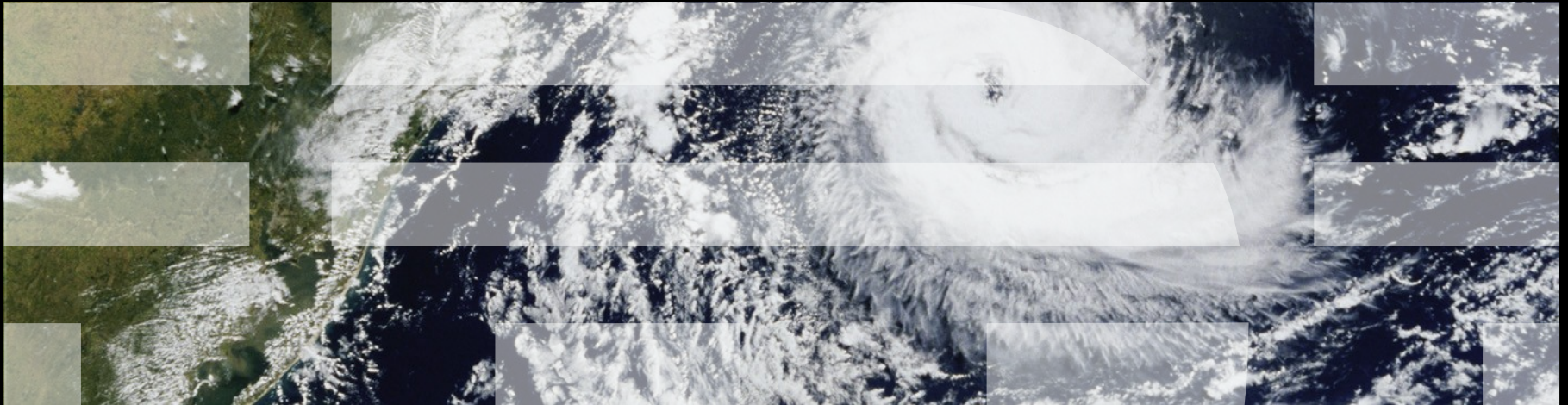
Paul E. McKenney, IBM Distinguished Engineer, Linux Technology Center

October 25, 2013



Scaling Talks

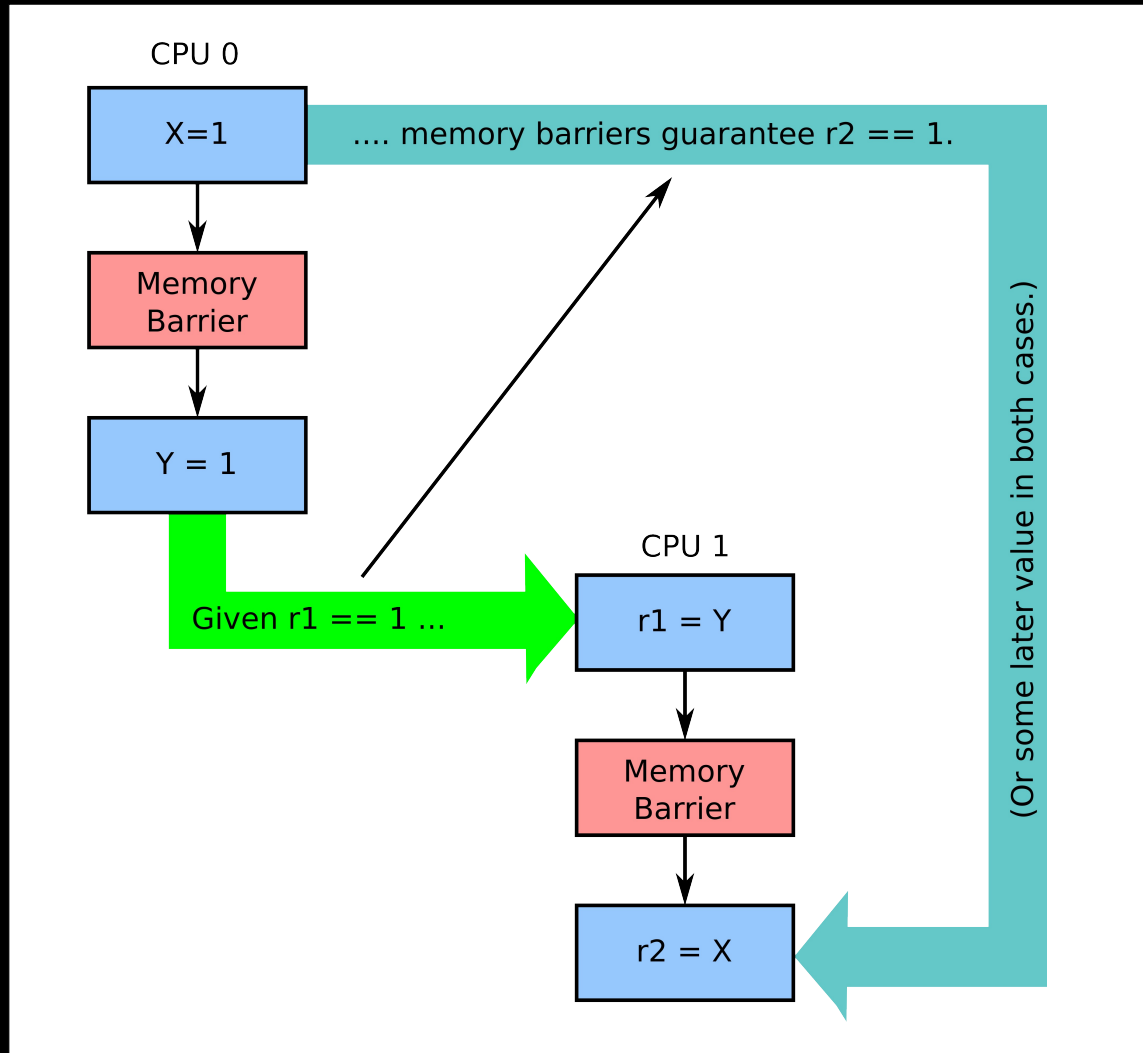
Linux Kernel Summit, Edinburgh, UK



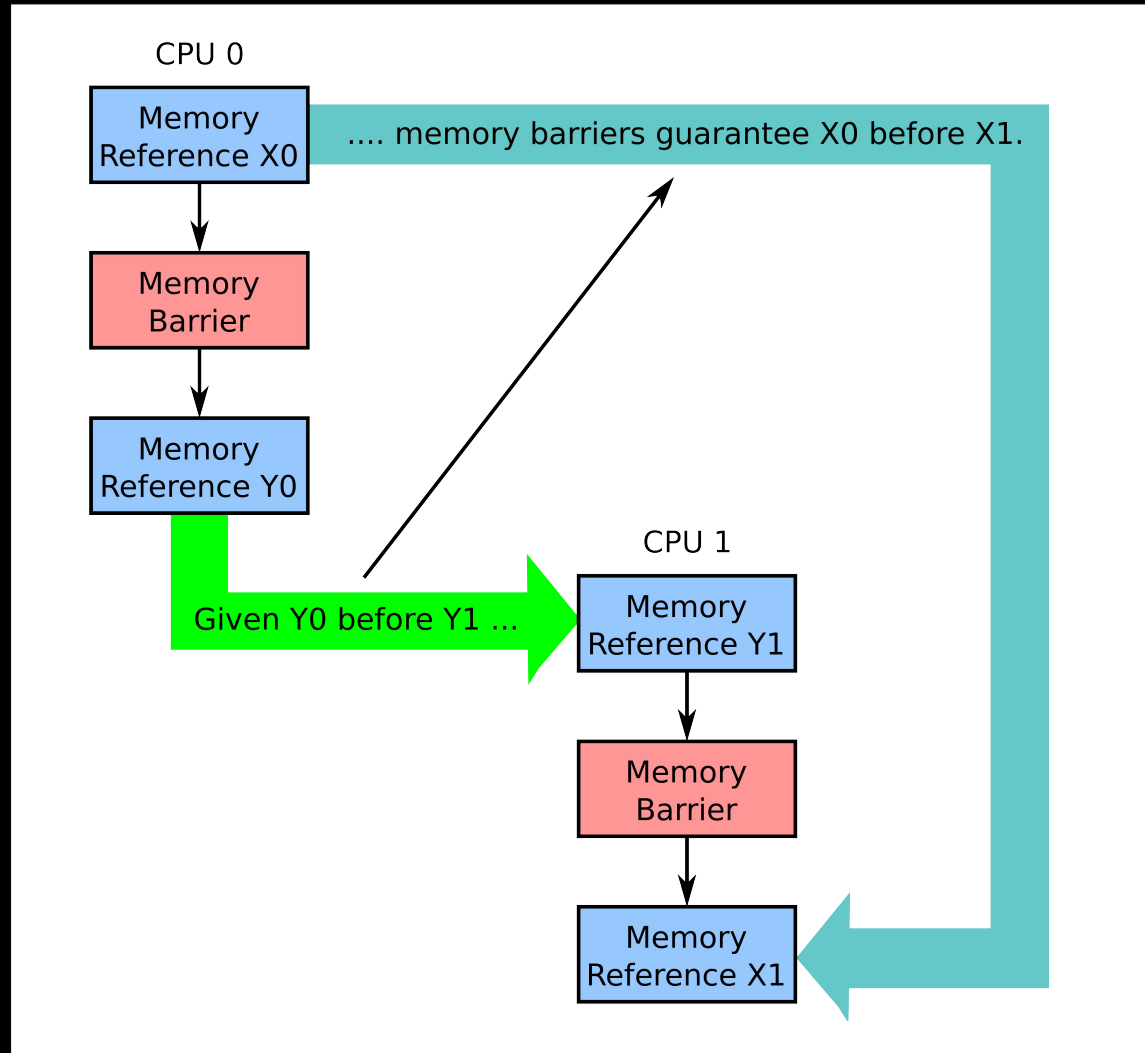
Overview

- A few short talks:
 - Memory barriers the easy way (this one)
 - Josh Triplett: Creating correct RCU data structures
 - Andi Kleen: Lock elision
 - Lai Jiangshan: Overview of SRCU
- These will not be complete descriptions
 - More detailed discussions in this afternoon's hacking session
- Lots of other experts in the room!

Memory Barriers the Easy Way: Example



Memory Barriers the Easy Way: General Rule



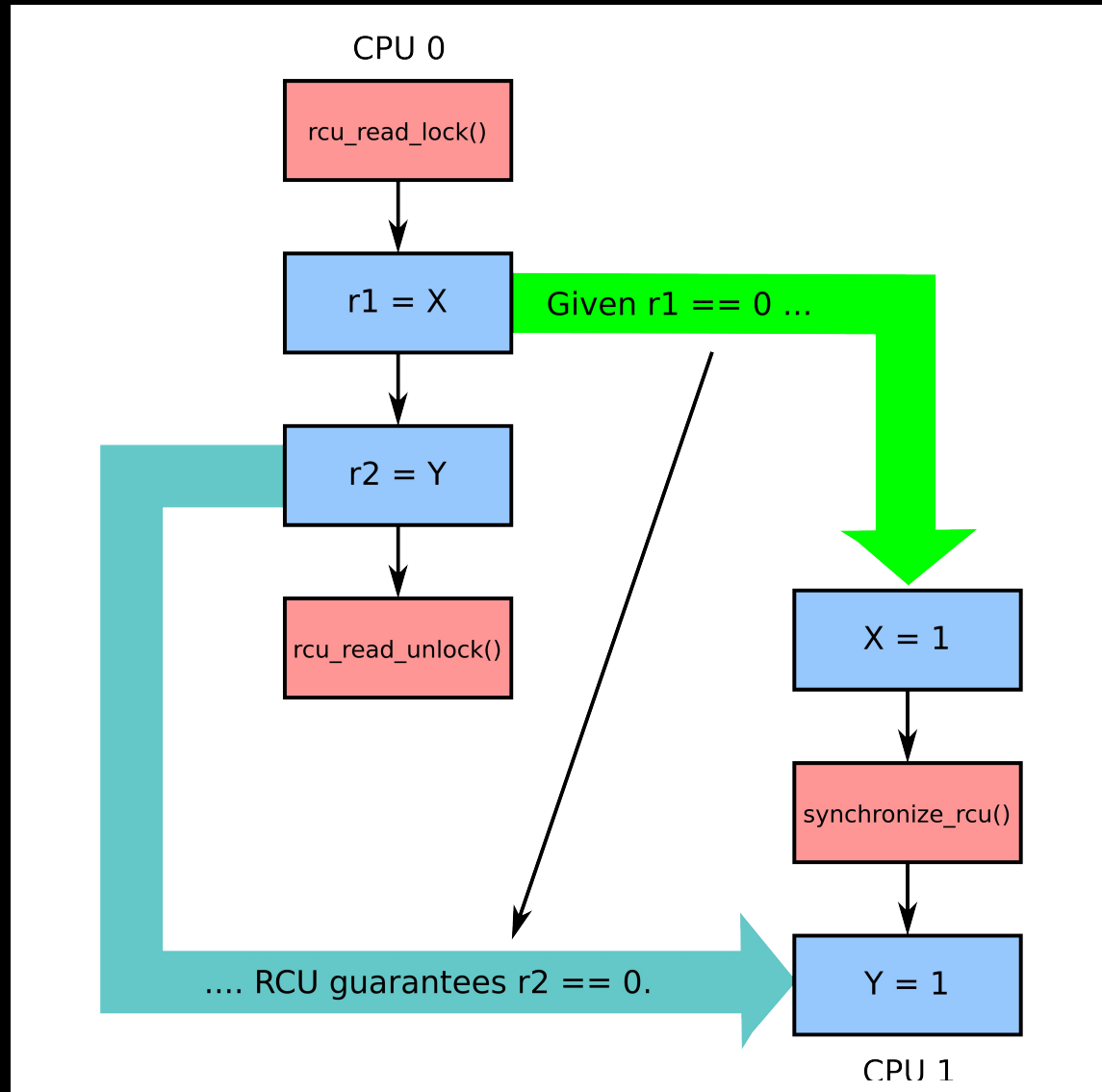
But Memory Barriers Are Expensive...

Can't we use something cheaper?

But Memory Barriers Are Expensive...

Can't we use something cheaper?
We can shift the costs...

Memory Barriers the Fast and Easy Way Using RCU



RCU As Barrier: Rule

- If any part of an RCU read-side critical section happens before the beginning of an RCU grace period...
 - ... all of that RCU read-side critical section happens before the end of that RCU grace period
- If any part of an RCU read-side critical section happens after the end of an RCU grace period...
 - ... all of that RCU read-side critical section happens after the beginning of that RCU grace period

But Grace Periods Are *Really* Expensive...

But Grace Periods Are *Really* Expensive...

... and Josh will show us when we really need them and when we don't!

Or:

“Constructing correct RCU data structures”

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